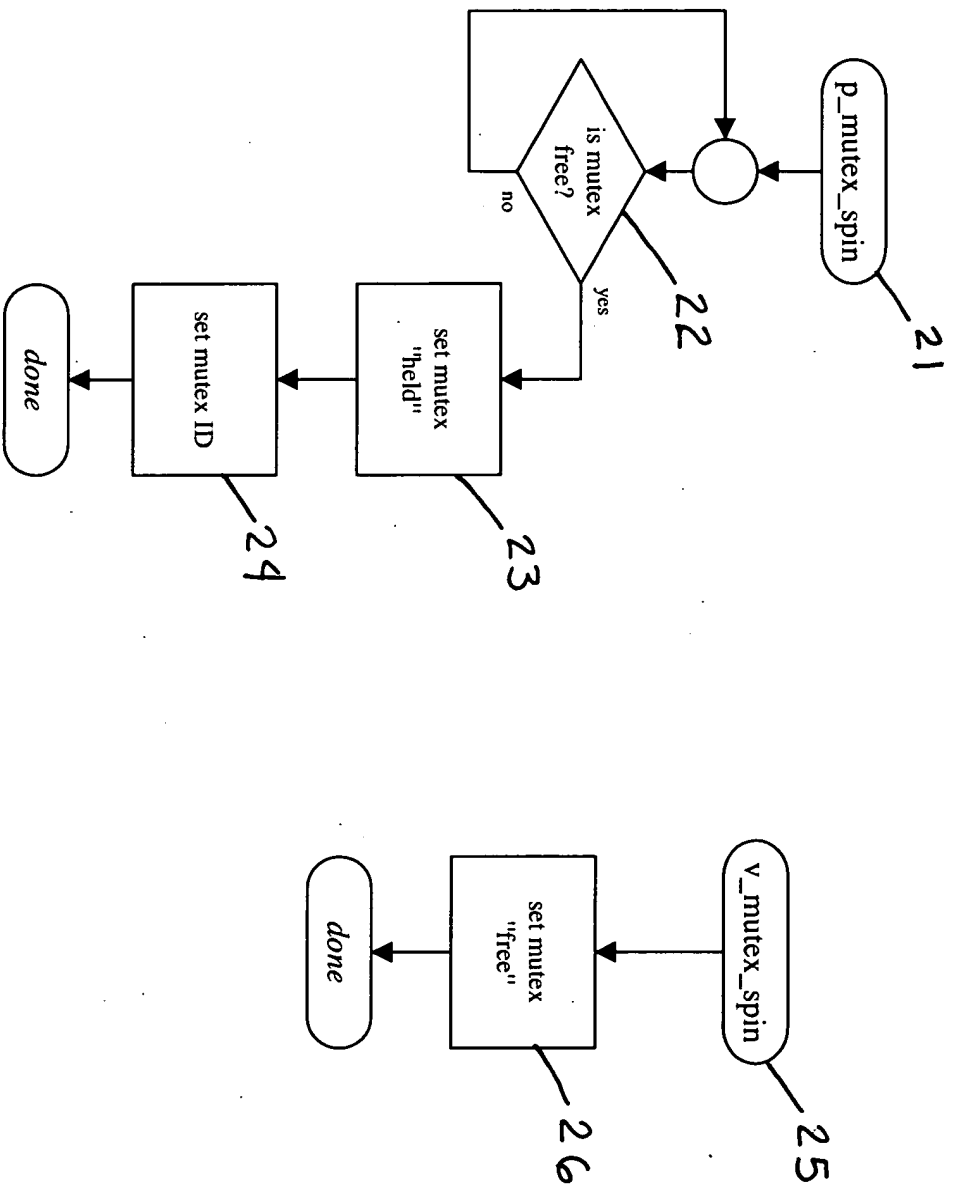


Fig. 1

FIG. 1 is a block diagram of a system architecture.



2
Figure X: Spin Mutex

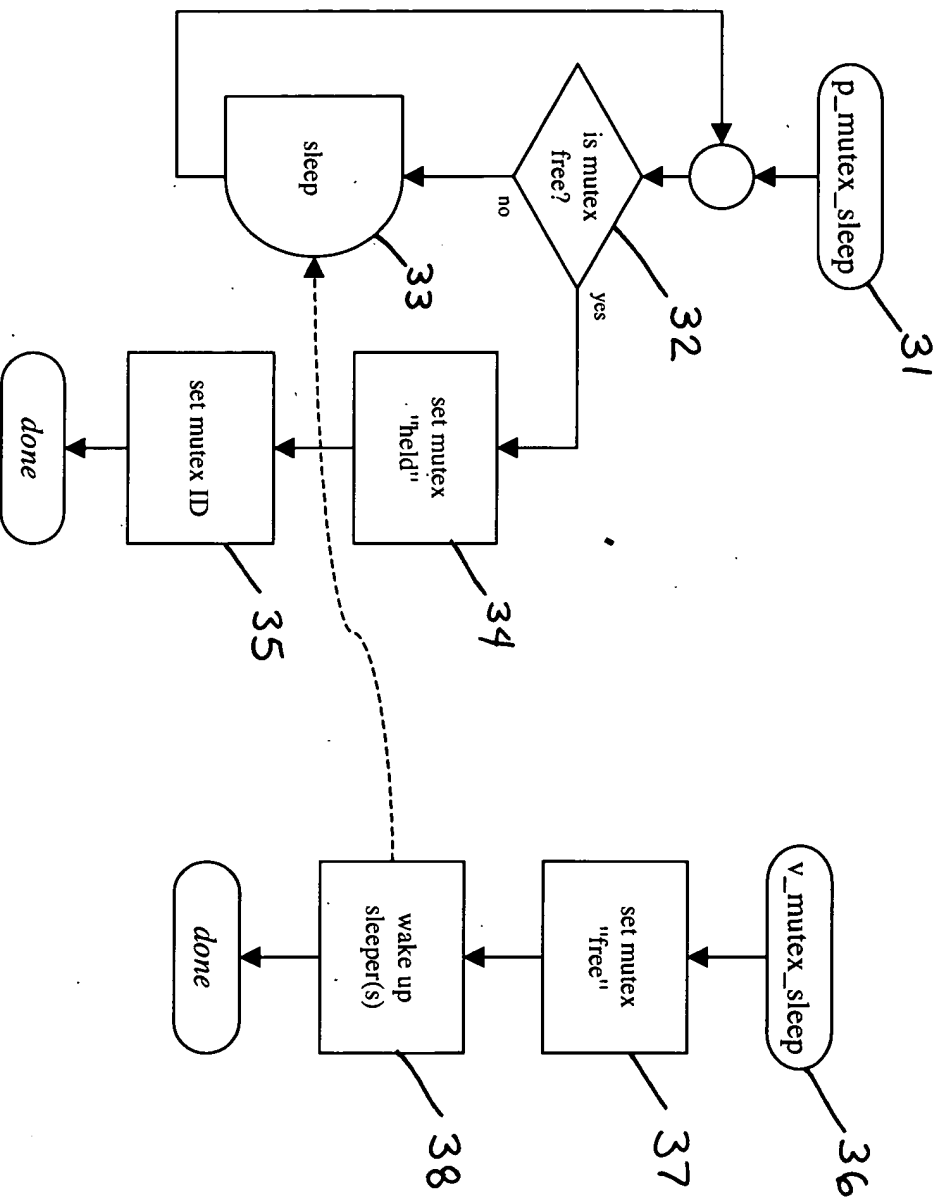
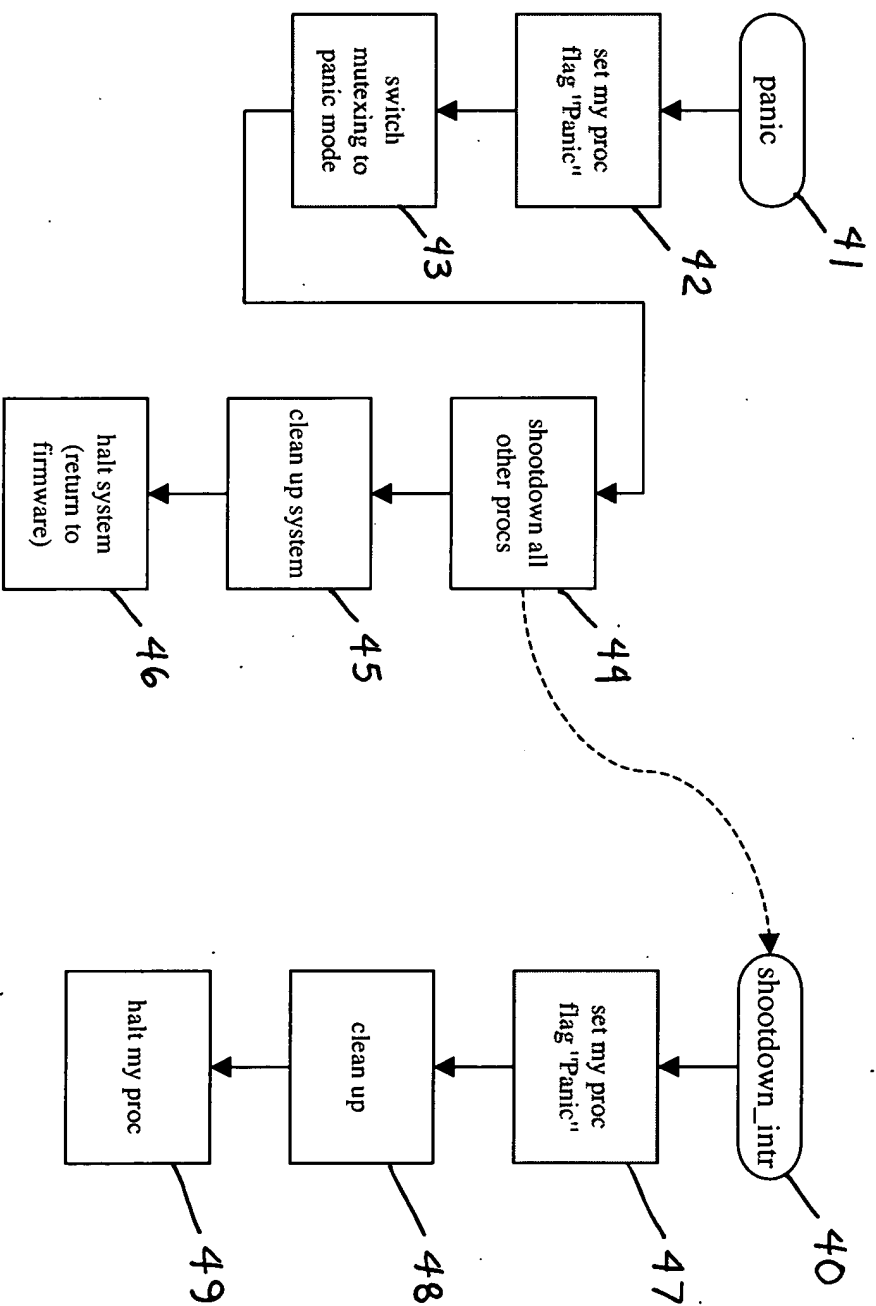
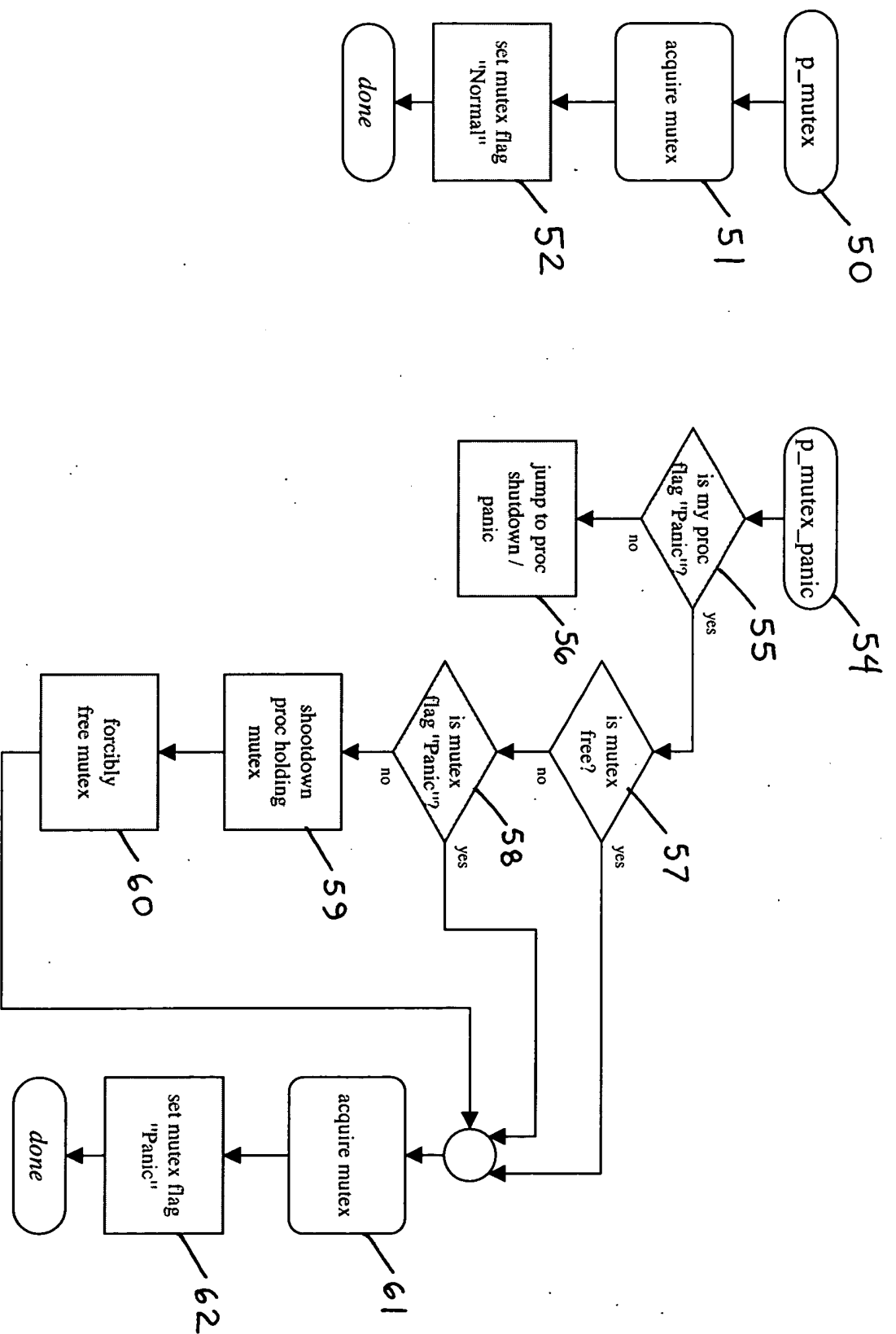


Figure 2a: Sleep Mutex



4
Figure 3: Panic Handling



5
Figure 4: Mutex Handling, Normal vs. Panic

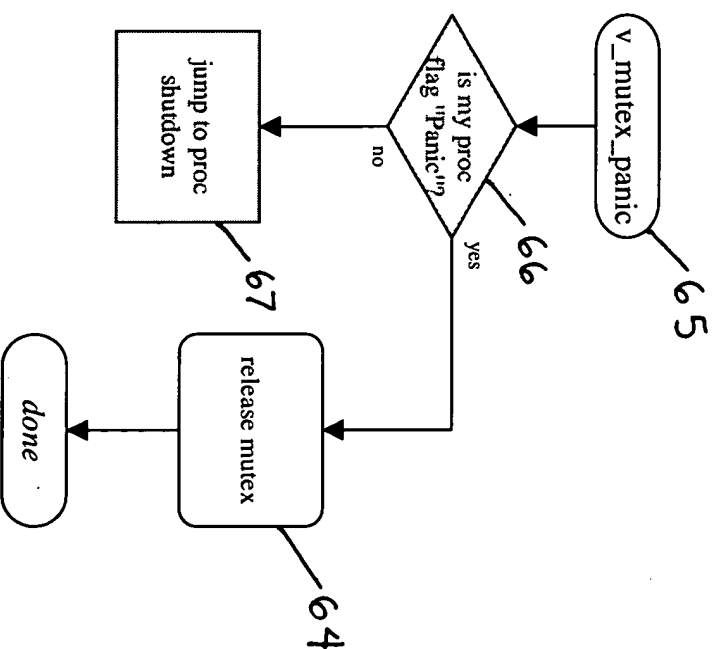
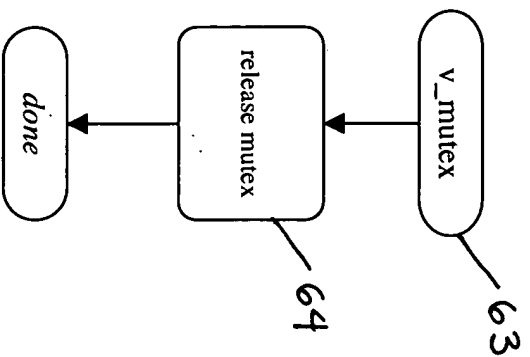


Figure 5: Mutex Handling, Normal vs. Panic (cont.)

1002

1008

1006

1004

1010

1012

FIG. 7

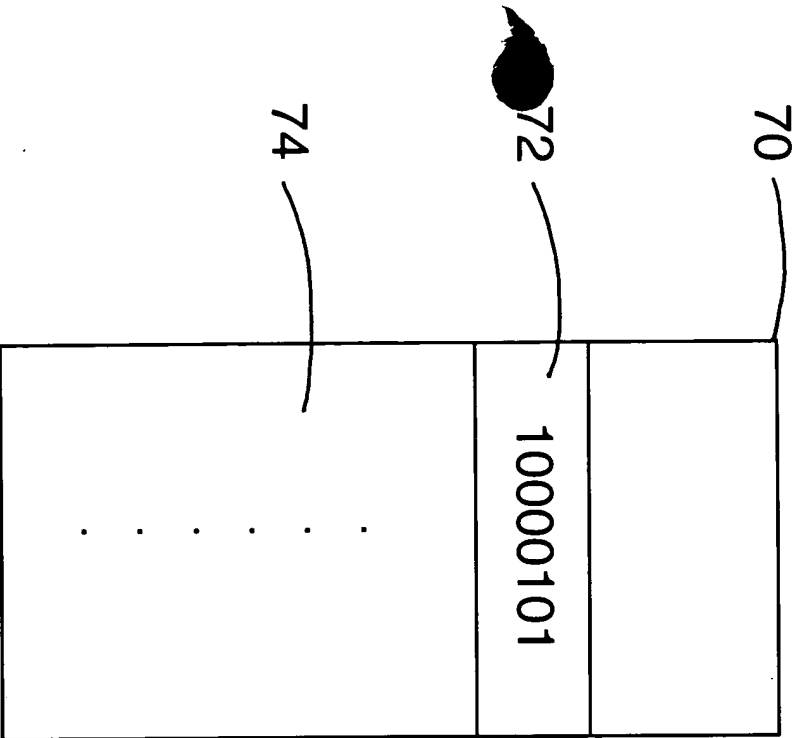


Fig. 8A
Pre-panic mutex

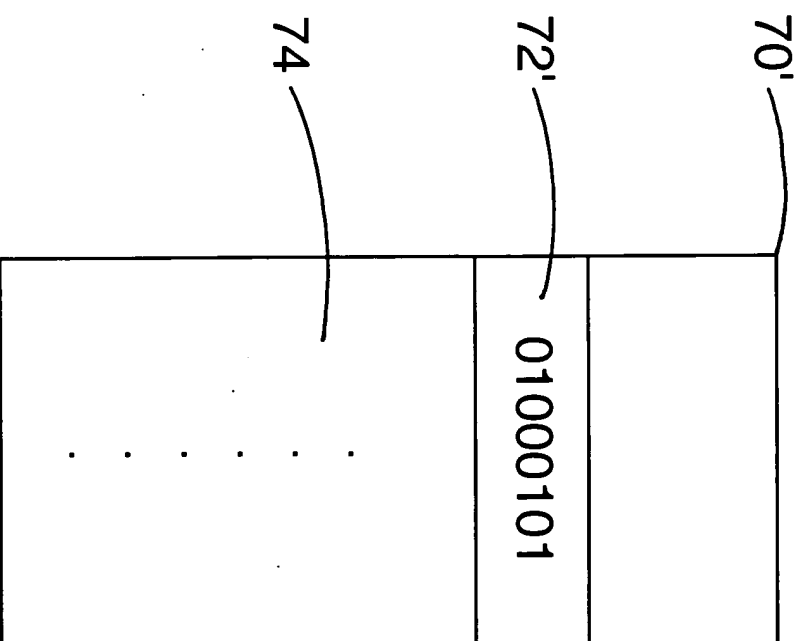


Fig. 8B
Post-panic mutex